

FIG. 1  
PRIOR ART

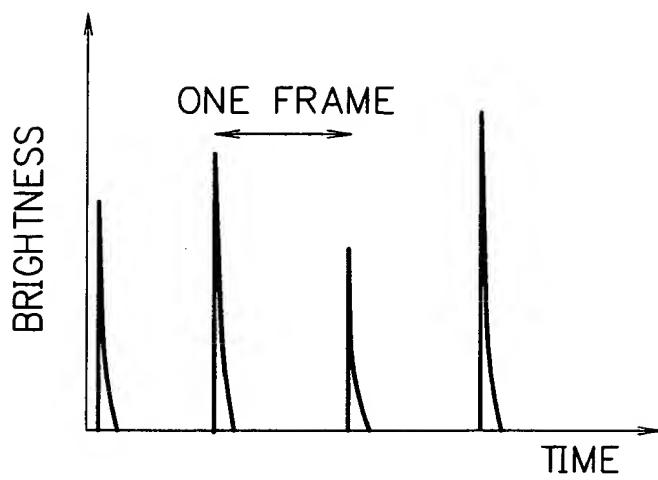
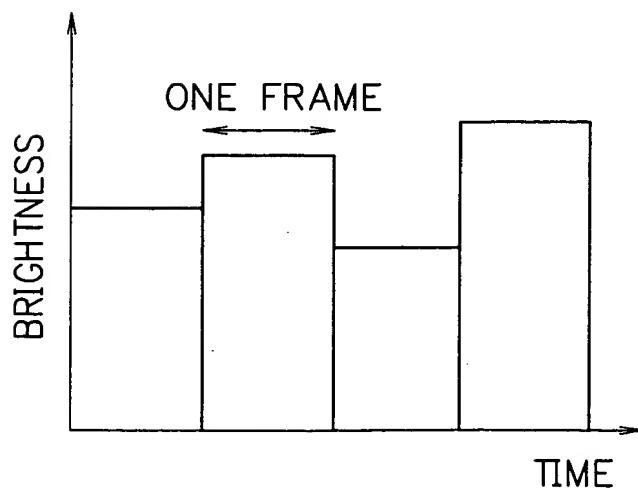
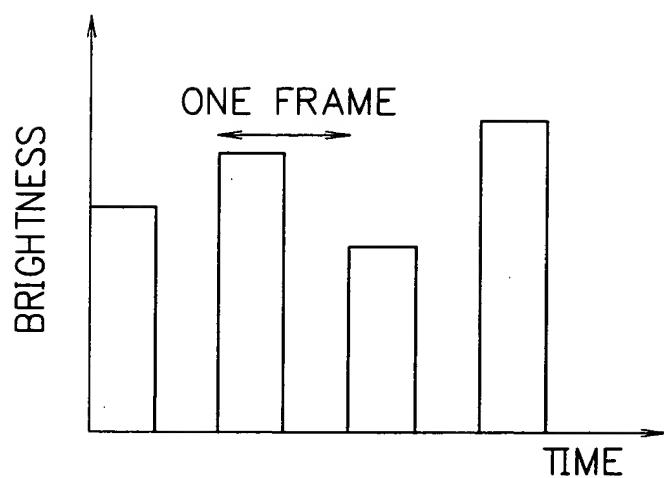


FIG. 2  
PRIOR ART

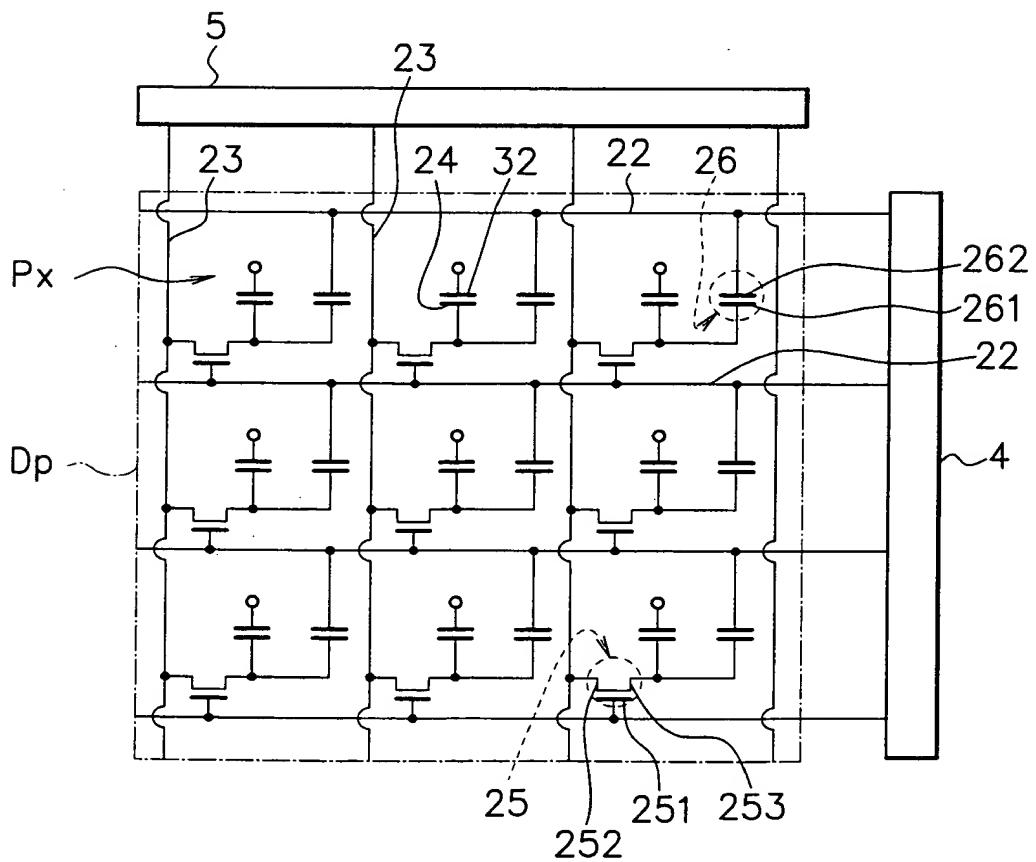


F I G. 3

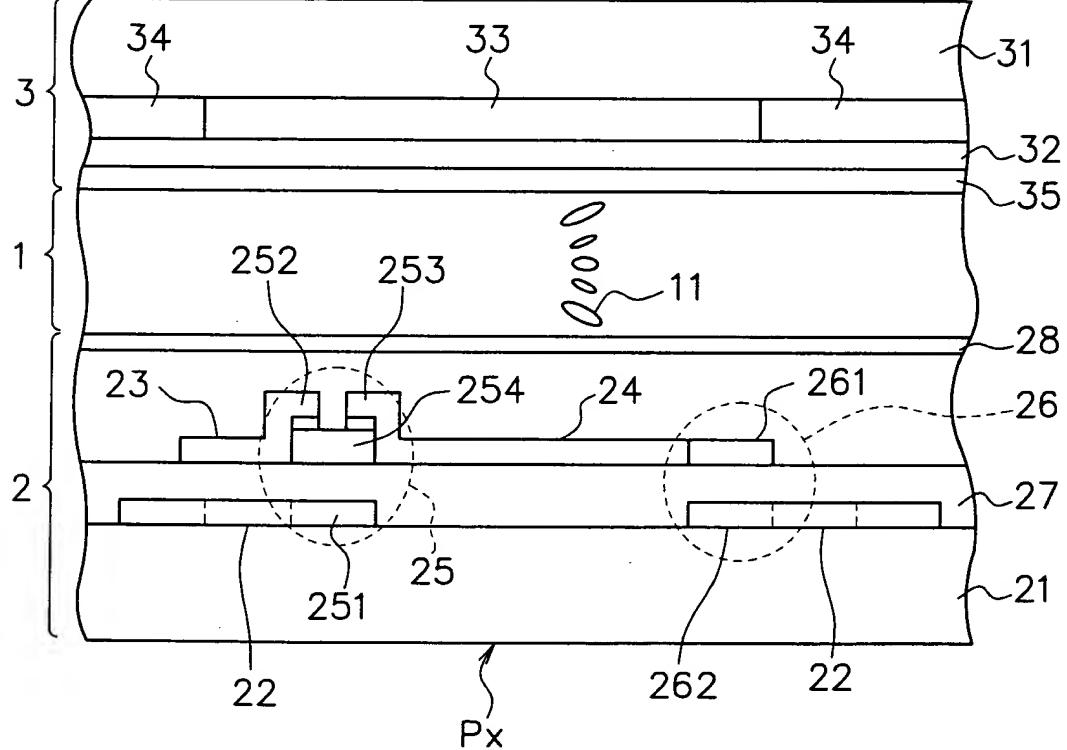
PRIOR ART



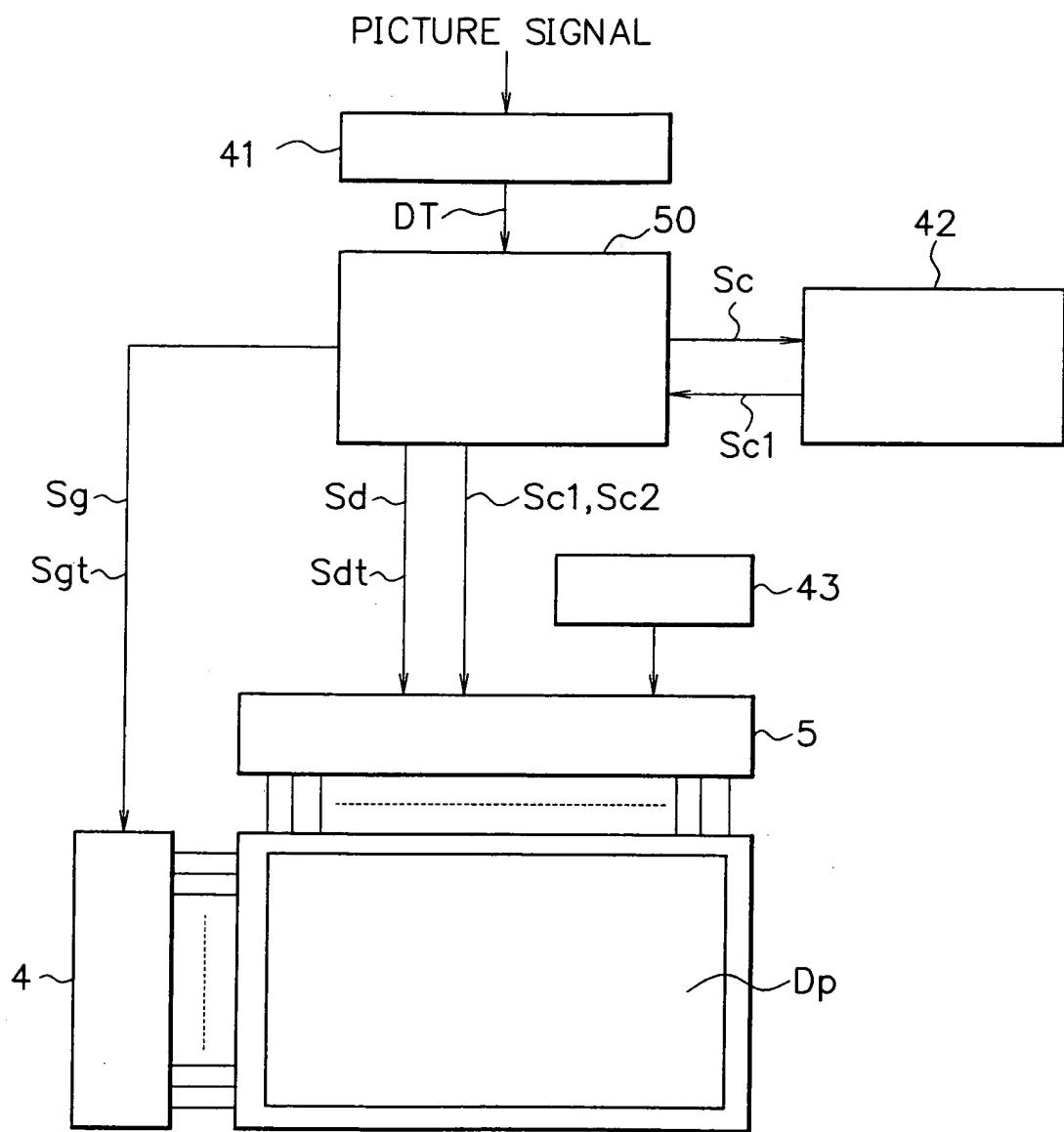
F I G. 4



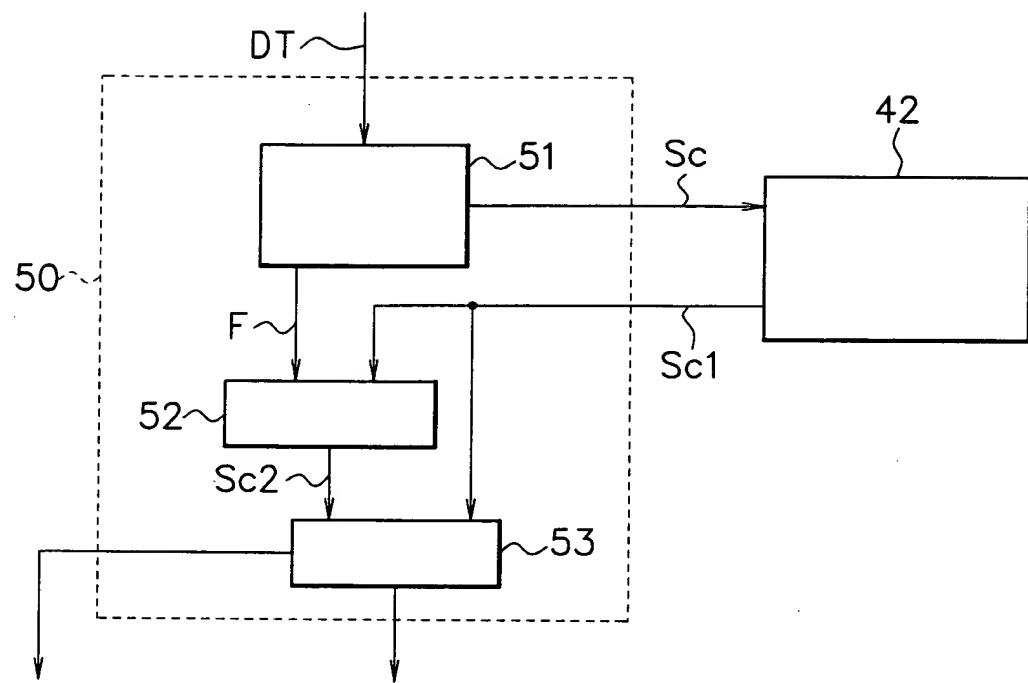
F I G. 5



F I G. 6



F I G. 7



# F 1 G. 8

A/D CONVERTER 41

CONTROLLER 50

RESOLUTION  
JUDGING CIRCUIT 51

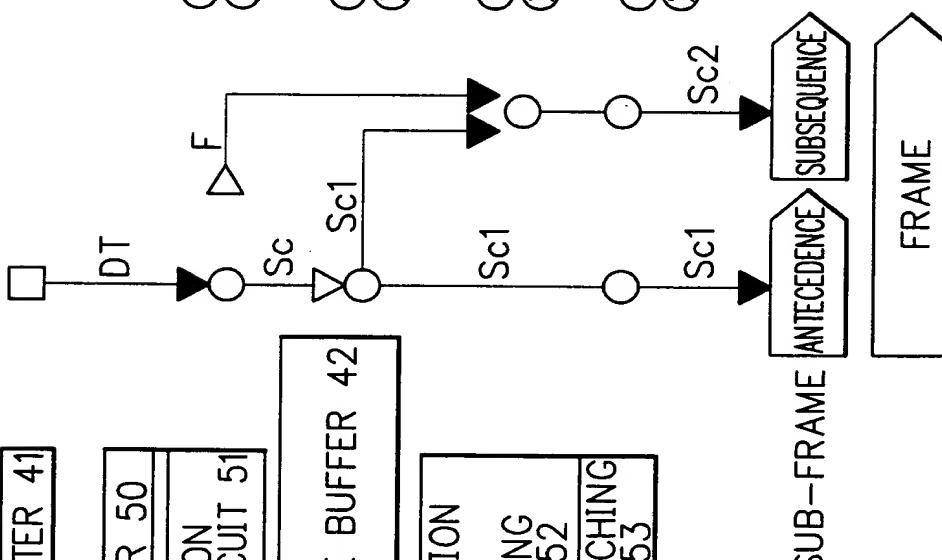
FRAME BUFFER 42

ATTENUATION  
GENERATING  
CIRCUIT 52

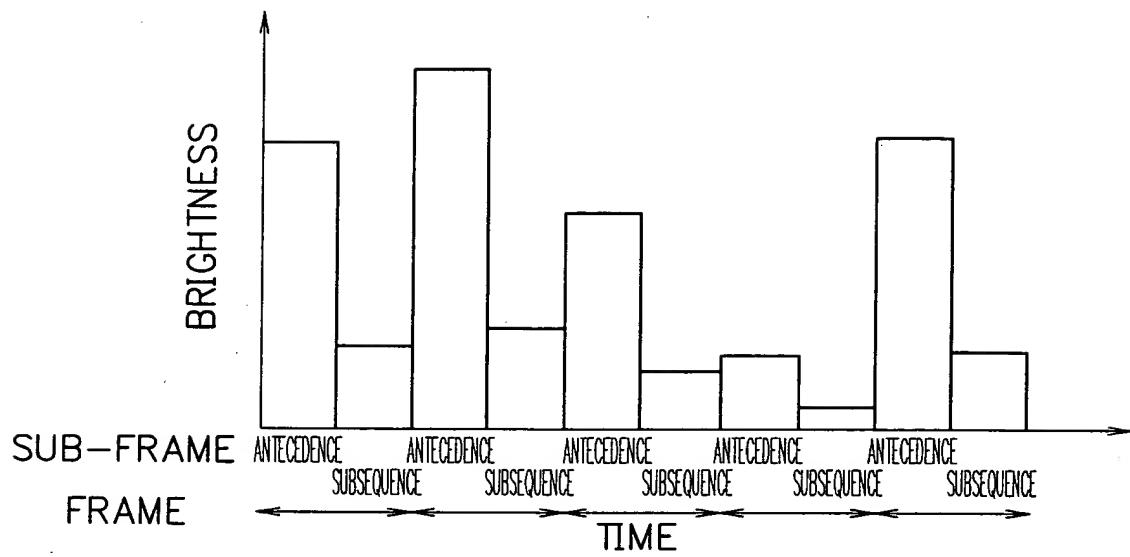
SIGNAL SWITCHING  
CIRCUIT 53

DIGITALIZATION OF  
RGB PICTURE SIGNAL

- ① DECIDING BRIGHTNESS
- ② GENERATING ATTENUATION COEFFICIENT  $F$
- ① SAVING SIGNALS FOR ONE FRAME
- ② GENERATING SUB-FRAME BY READOUT WITH DOUBLE SPEED
- ① DIVIDING
- ② GENERATING ATTENUATION SIGNAL  $Sc_2$
- ① SWITCHING SUB-FRAME
- ② OUTPUTTING SIGNALS FOR SUB-FRAME



F I G. 9



F I G. 10

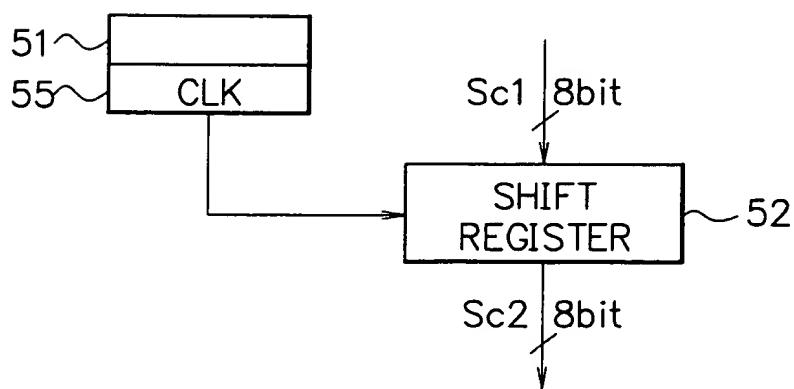
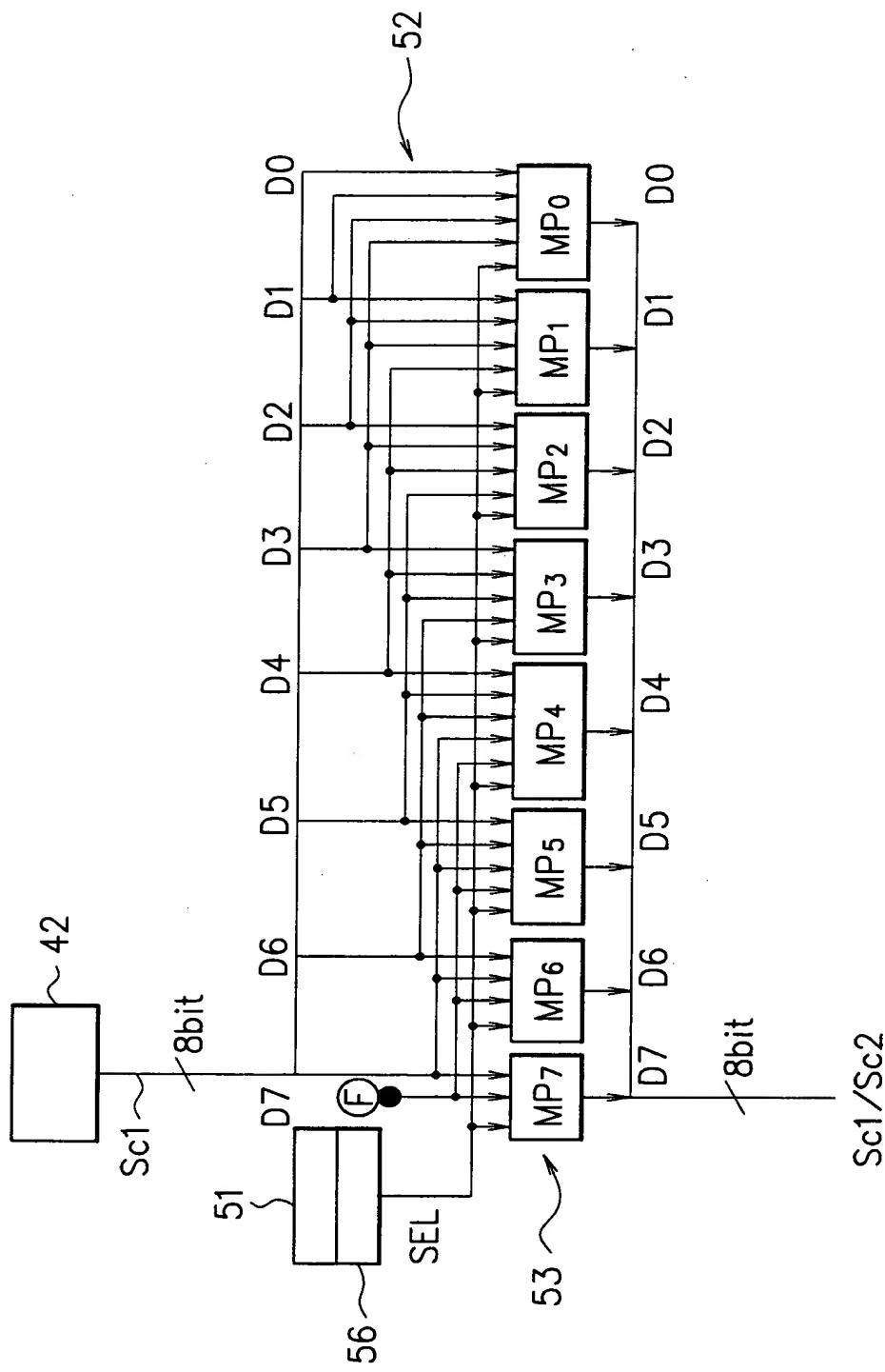
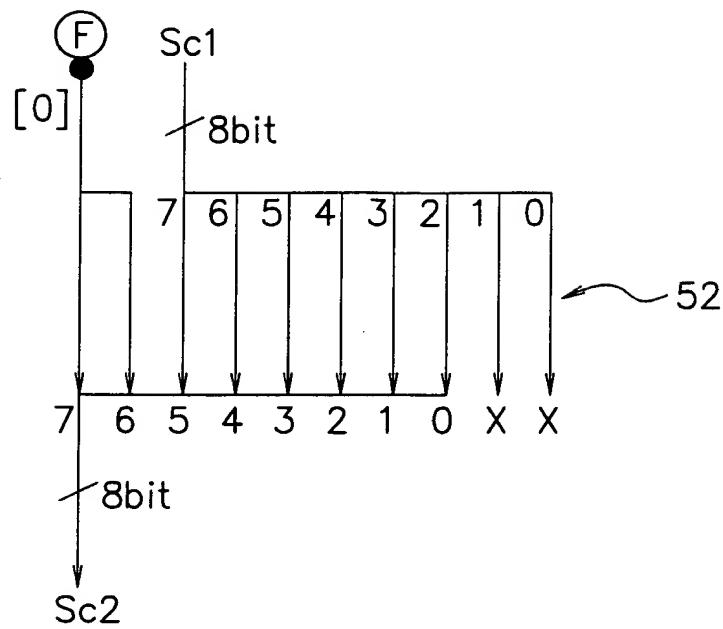


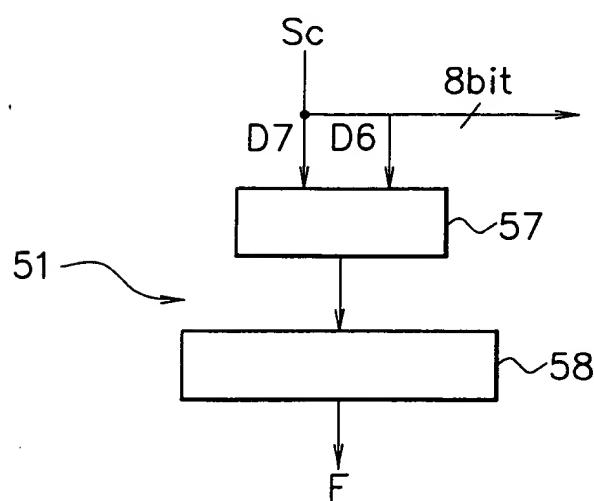
FIG. 11



F I G. 12



F I G. 13



F I G. 14

